

FIG. 3

Phase inversion  
(with respect to 18a, 19a)  
Track end

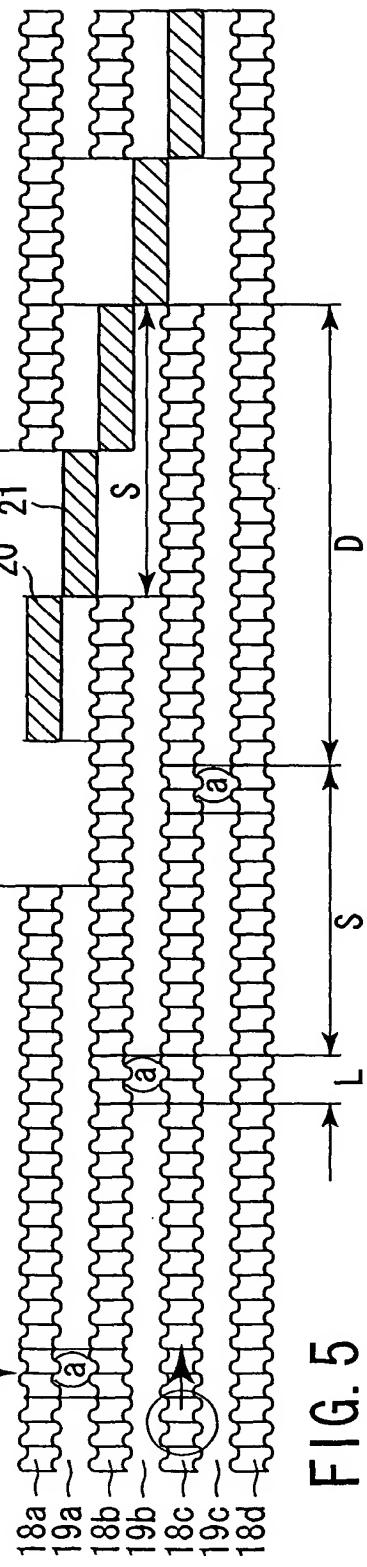
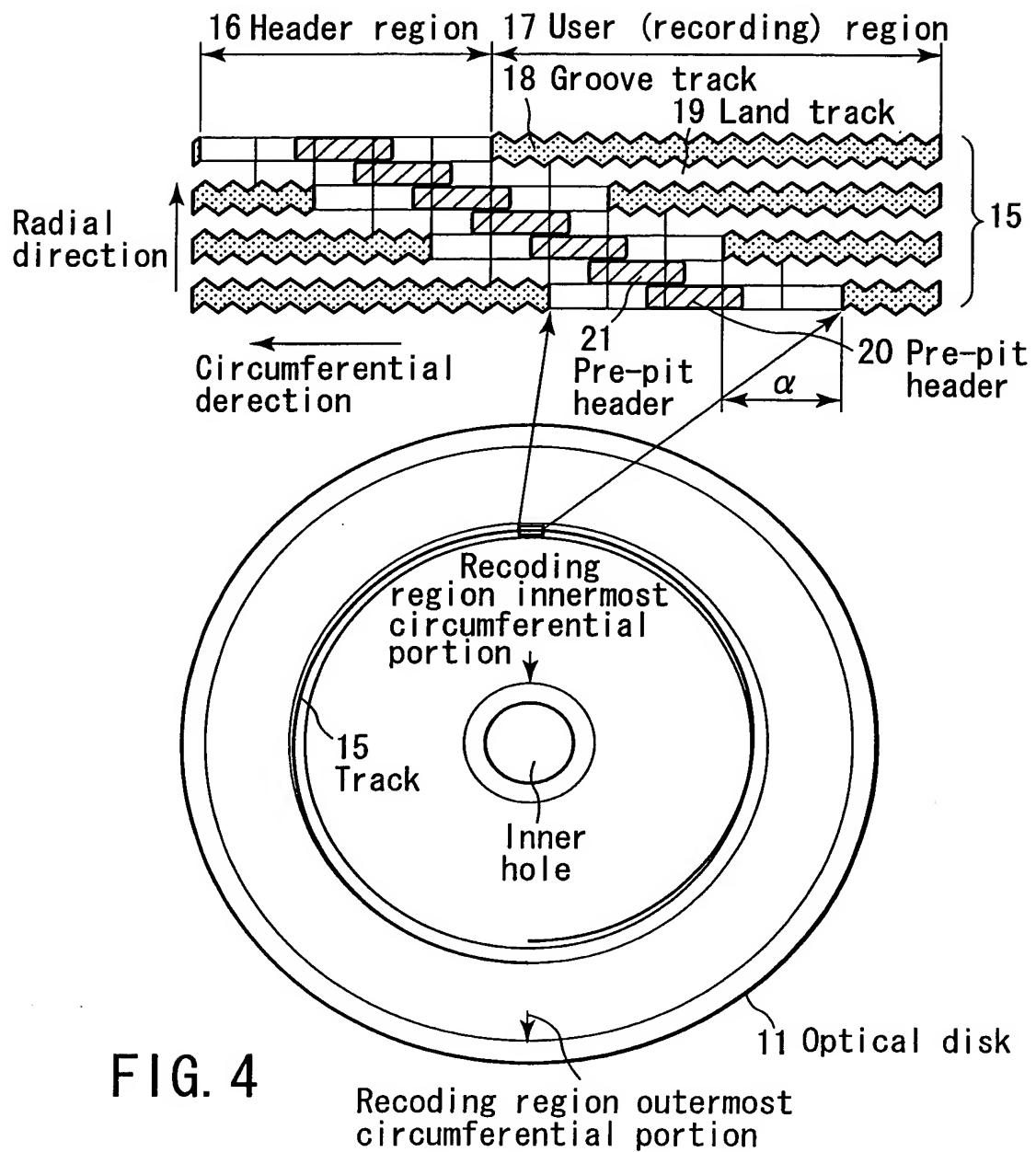


FIG. 5



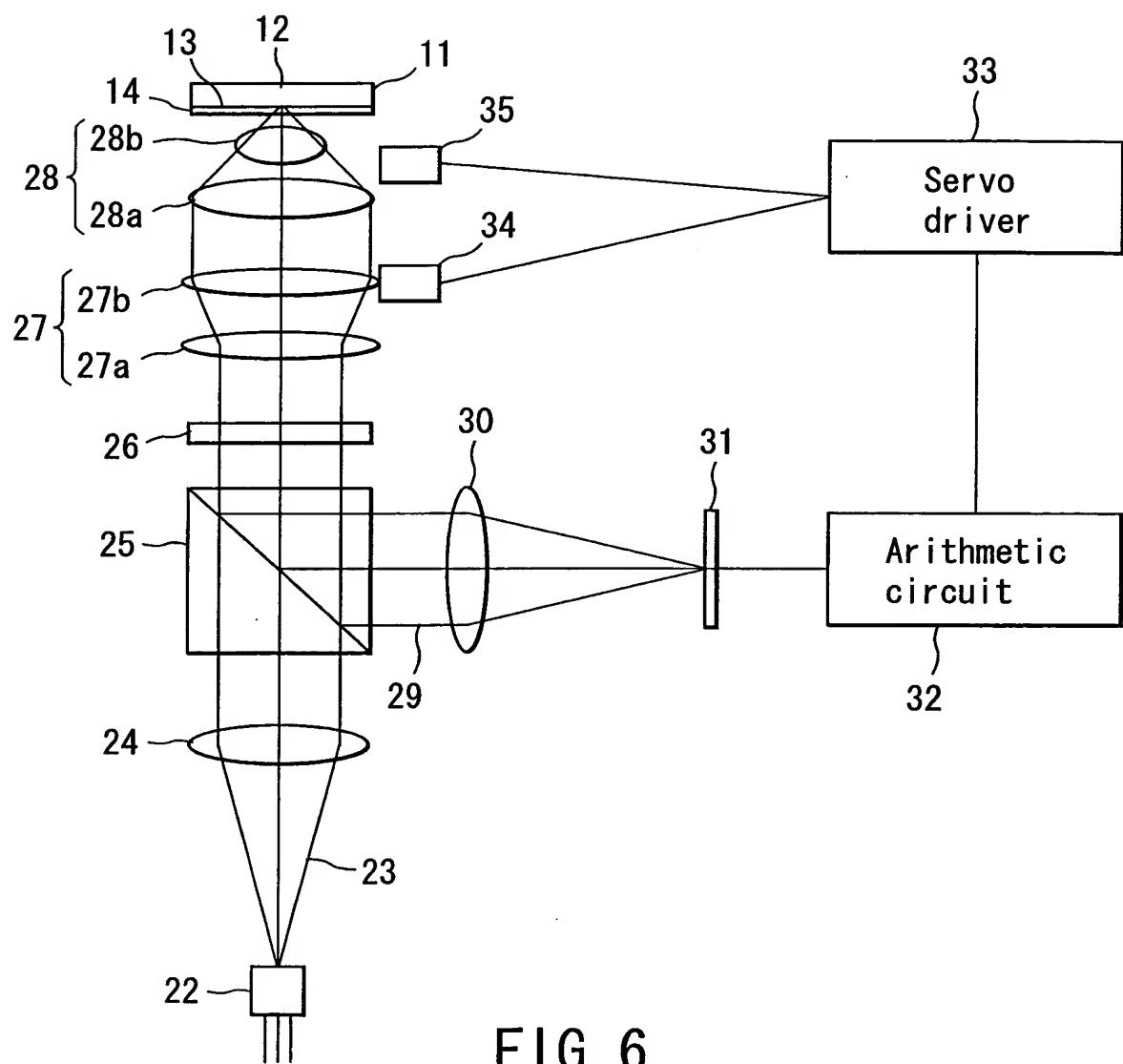


FIG. 6

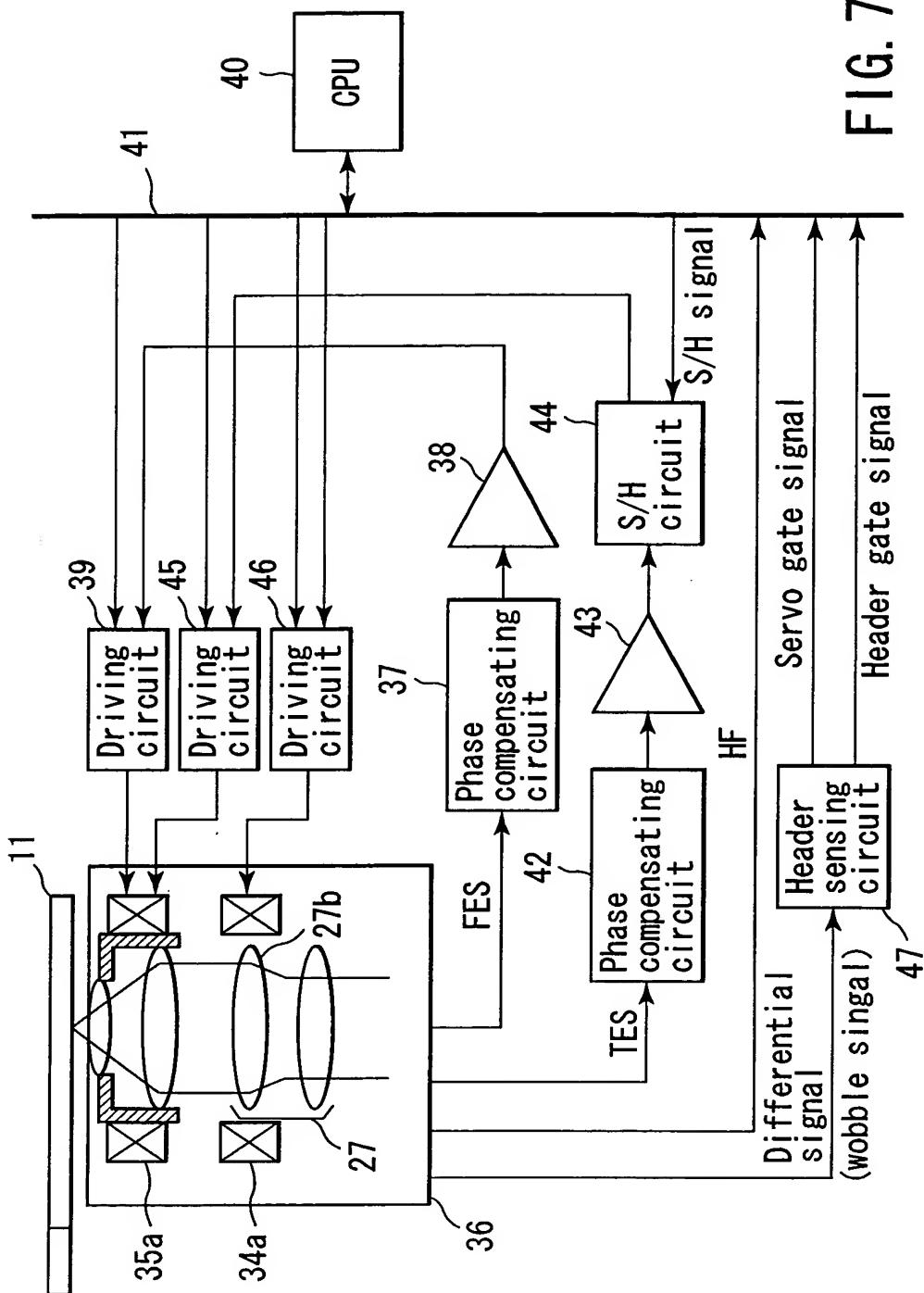


FIG. 7

Differential signal (wobble signal)  
Header sensing circuit  
Header gate signal

Servo gate signal  
Header gate signal

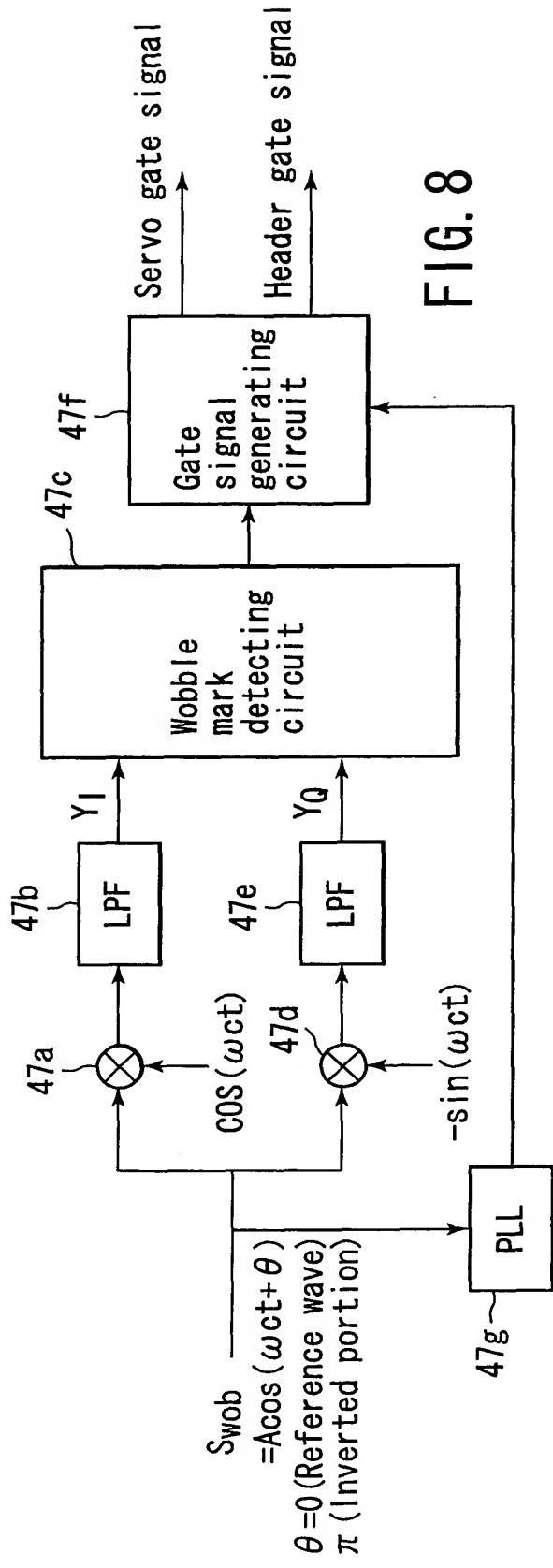


FIG. 8

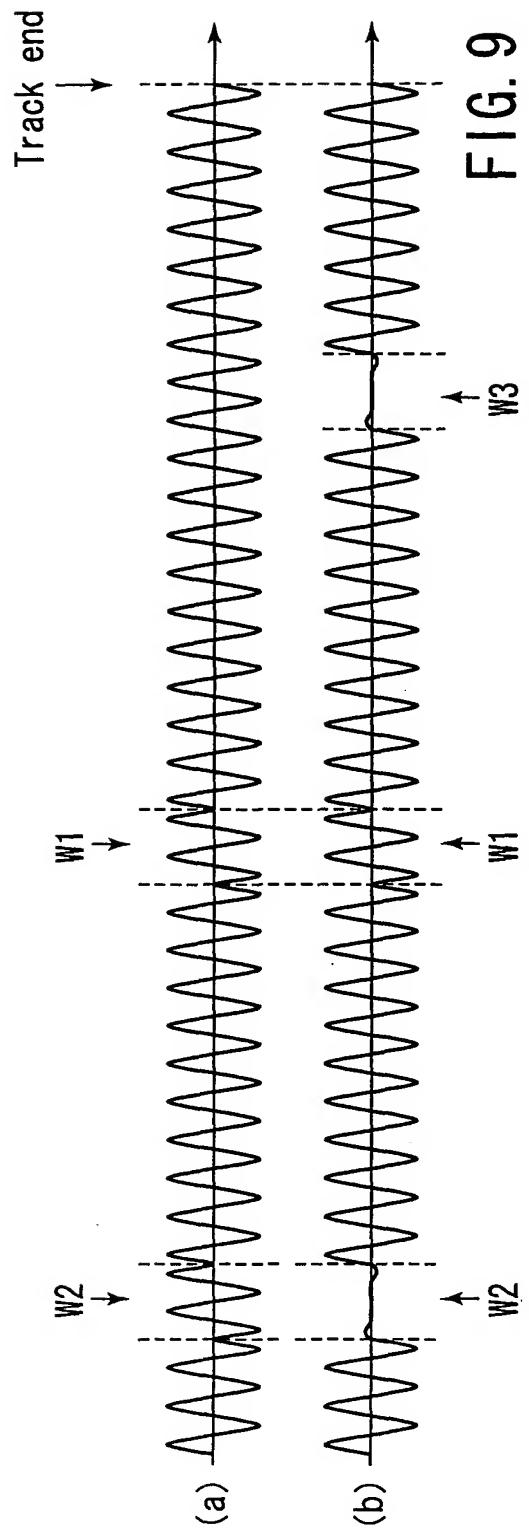


FIG. 9

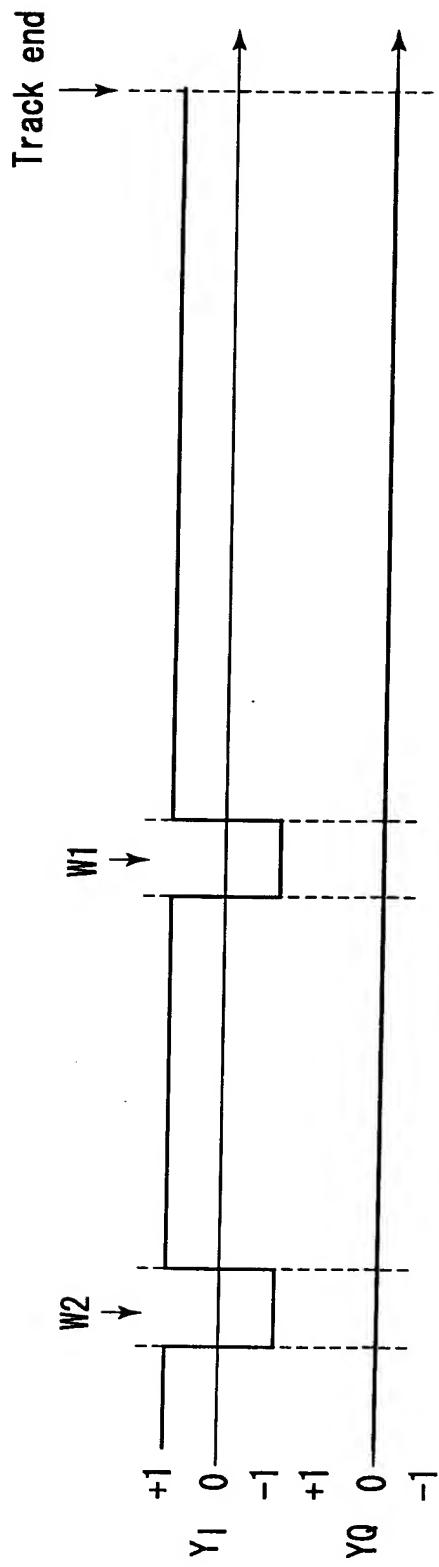


FIG. 10A

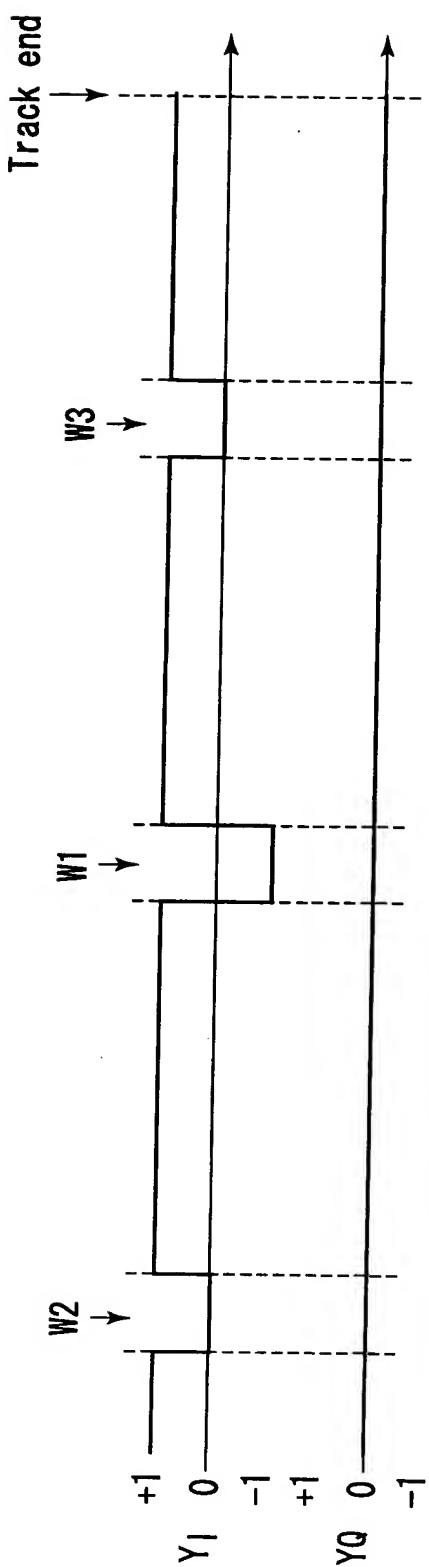


FIG. 10B